

CURRICULLUM VITAE



Name : **Dr. Sagarika Das**
Husband's name : Mr. Chandan Das
Date of Birth : December 22, 1978
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Block 1, 803, Jains Prakrithi, 63 Kanakpura Road, Jayanagar, 7th Block, Bengaluru-560070

WORK EXPERIENCE:

Academia: Teaching

- | | |
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| 1. Associate Professor, HIT, Haldia | July 2022 – Till Date |
| 2. Associate Professor, NMIT Bengaluru | July 2019 - April 2022 |
| 3. Sr Assistant Professor, COER, Roorkee | August 2015 - June 2018 |
| 4. Assistant Professor, BPPIMT, Kolkata | June 2009 - July 2015 |
| 5. Sr. Lecturer GNIT, Kolkata | January 2009 - June 2009 |
| 6. Sr. Lecturer, NCET, Kanpur | January 2008 - October 2008 |
| 7. Lecturer, Amity University, Noida | June 2007 - December 2007 |
| 8. Lecturer, GNIT, Kolkata | July 2006 - May 2007 |
| 9. Lecturer, HIT, WB | July 2001 - June 2004 |

Research Associate:

Jadavpur University, Kolkata

October 2008 - December 2008

EDUCATION:

➤ Awarded **PhD Thesis** on the topic “**Analytic Approach to realization of some tunable selective circuits using CCCS type building blocks**” in **Jadavpur University**, under the guidance of Prof. R. Nandi and Prof. P. Venkateswaran on 28th January, 2016

➤ **Masters of Engineering [M.E.- Electronics & Tele Communication]** 2006

Jadavpur University

Specialization: **Communication Engineering**

Secured 82.15%

➤ **Bachelor of Engineering [B.E.- Electronics & Communication]** 2000

NIT Durgapur (formerly known as REC Durgapur)

Secured 71.4%

➤ **Higher Secondary Education [W.B.C.H.S.E]** 1996

Hoomgarh Chandabila Higher Secondary School

Secured 74.1%

➤ **West Bengal Board of Secondary Education [W.B.B.S.E]** 1993

Goaltore Girls' High School

Secured 69.7 %

QUALIFICATION AND TRAINING:

1. All India Rank **479** in Graduate Aptitude Test for Engineers (**GATE**) 2000
Electronics and Communication Engineering
Score: 94.54 percentile

2. ATAL FDP on Wearable devices 11.01.21 - 15.01.21

3. ATAL FDP on IOT 14.12.20 - 18.12.20

4. FDP on IP Rights and Patenting 05.10.20 - 09.10.20

5. Workshop on Internet of things using Raspberry Pi 02.10.20 - 04.10.20

6. FDP on Xilinx Vivado technique and Machine Learning algorithm
using PYNQ Framework

7. FDP on Internet of Things 16.07.20 - 18.07.20

8. Workshop on Analog and Digital VLSI design using open-source EDA and Inohmic Tech Methodologies 30.07.20 - 03.8.20
9. Online training programme on electronic system design flow using Orcad Spice, by Entuple Technologies

MEMBERSHIP OF PROFESSIONAL BODIES

Member, IEEE Circuit and Systems, since 2009

PUBLICATIONS:

Journal Publications:

1. R. Nandi, Mousiki Kar, and **Sagarika Das**, “Electronically Tunable Dual-Input Integrator Employing a Single CDBA and a Multiplier: Voltage Controlled Quadrature Oscillator Design” *Active and Passive Electronic Components*, volume 2009, Article ID 835789, 5 pages doi:10.1155/2009/835789,2009.
2. R. Nandi, S. Das, Mousiki Kar & **Sagarika Das**, “Active-R tunable integrators using a current differencing buffered amplifier”, *International Journal of Electronics*. vol. 97, No. 2, pp. 129–137, February 2010.
3. Palaniandavar Venkateswaran, Rabindranath Nandi, **Sagarika Das**” New Integrators and Differentiators Using a MMCC “*Scientific Research, Circuits and Systems*, 2012, pp. 288-294, 2012.
4. Rabindranath Nandi, Palaniandavar Venkateswaran, **Sagarika Das**,” Selective Filters and Tunable Sinusoid Oscillator Using a CDBA “*Scientific Research Circuits and Systems*, 2013, 4, pp. 227-232,2013.
5. P. Venkateswaran, R. Nandi, **Sagarika Das**,” Voltage Controlled Integrator and Linear Quadrature-VCO Using MMCC”, *Buletin Teknik Elektro dan Informatika (Bulletin of Electrical Engineering and Informatics)*, vol. 2, No. 2, June 2013, pp. 111~116 ISSN: 2089-3191.
6. R. Nandi, **Sagarika Das** and P. Venkateswaran, “Ideal Grounded Supercapacitor Using A DVCCTA” *Journal of Telecommunications*, volume 22, issue 2, pp 7- 11, November 2013.
7. **Sagarika Das**, P. Venkateswaran and R. Nandi,” Voltage Tunable All pass Filters Using MMCC,” of *Active and Passive Electron Devices*, vol.9, pp.223 – 234, 2014.

8. R. Nandi, **Sagarika Das** and P. Venkateswaran “Floating lossless immittance functions using DVCCTA”, “International Journal of Electronics Letters”,
<http://dx.doi.org/10.1080/21681724.2014.966772>, 2014
9. “Electronically Tunable Differential Integrator: Linear Voltage Controlled Quadrature Oscillator”, Rabindranath Nandi, Sandhya Pattanayak, Palaniandavar Venkateswaran, and **Sagarika Das**, International Scholarly Research Notices Volume 2015, Article ID 690923, 7 pages. <http://dx.doi.org/10.1155/2015/690923>
10. **Sagarika Das**, Baswakiran, Shweta Dasar, Jagdeesha, Srinivasa Rao, “Women security System”, International Journal of Engineering Research & Technology (IJERT) ISSN: 2278-0181 Vol. 10 Issue 07, July-2021
11. **Sagarika Das**, Himanshu Anand, Suchandra Das “Automatic Irrigation Using IOT And Nutrient Dispenser” <https://doi.org/20.14118/v74.2024.102306>, Vol 74 (Oct-Nov) (Scopus Indexed)

Conference Publications:

National conference:

1. S.Kauri, R.Nandi “Results on the survey of some MANET routing protocols”, NASDEC2, BIT Mesra ,Ranchi, November 2006.
2. Study and measurement of different human body parameters affected by stress using stress meter” Sagarika Das. Rashmita Mishra, Susmita Biswas.CSTS-2012, BPPIMT, KOLKATA, PP NO C5-C11

International Conference:

1. “Design and Implementation of Super Capacitor Energy Storage in Satellite Applications”, Jayavrinda Vrindavanam, **Sagarika Das**, Vinay S, Linosh Kurian Malayil , Sunita Rani Nahak , Brenda Supriya A 2020 IEEE International Conference for Innovation in Technology (INOCON) Bengaluru, India. Nov 6-8, 2020.IEEE Explorer (Scopus Indexed)

ADDITIONAL RESPONSIBILITIES:

1. Department Placement Coordinator, COER, Roorkee
2. Ranking Coordinator (NIRF, ATAL, Times Etc), NMIT, Bengaluru, Autonomous Institute
3. PG Coordinator for MTech in Embedded and VLSI system, NMIT, Bengaluru

4. Department representative in personality development training, NMIT Bengaluru
5. FDP/ Internship/ Webinar organizer in collaboration with different industries
6. Faculty advisor to UG/PG students

RESEARCH ACTIVITY

1. Associate with VLSI research team
2. Initiated hardware circuit design and implementation Lab
(Presently working with sensor based low signal)

COURSERA CERTIFICATIONS:

1. AI for Everyone organized by DeepLearning.AI, grade: 95
2. Machine Learning Specialisation consisting of 3 courses,
organized by DeepLearning.AI and Stanford Online
 - a) Supervised Machine learning: Regression and Classifier, grade: 100
 - b) Advanced learning Algorithm, grade: 100
 - c) Unsupervised Learning, Recommenders and Reinforcement Learning, grade: 99
3. Python for Data Science, AI & Development organized by IBM, grade: 93