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Address: Haldia ,West Bengal,India - 721657

Expertise

Telecommunications

Digital Microfluidic Bio-Chip, Embedded System

Work experience

1. Haldia Institute of Technology 2011 — Present

Assistant Professor
Purba Medinipur

2. Balasore College of Engineering and Technology, (BCET), Balasore 2007 — 2011

Assistant Professor
Balasore

Education

1. Ph.D - 2024

Mansarovar Global University

M.Tech - 2010

Haldia Institute of Technology

3. B.E. - 2005

Seemanta Engineering College

4. Diploma - 2002

Terna Polytechnic College

Honours and Awards

1. GATE Qualified (AIR-1354) - 2010

GATE

Membership In Professional Bodies

1. Institute Of Research Engineers and Doctors (IRED) (Membership ID: AM20211014637), 2021

Associate Member

2. International Association of Engineers (IAENG) (Membership ID: 168259), 2016

Associate Member

Membership In Committees

Patent

2.

1. **The sentimental based disease estimation and health risk prediction system**

Inventor(s): Rajrupa Metia, Santanu Maity, Razia Sultana, Amit Bhattacharya, Assignee: Haldia Institute of Technology

Published: 2022-07-08

2. **EDGE AI-ENABLED IOT SYSTEM FOR REAL-TIME ANOMALY DETECTION AND PREDICTIVE MAINTENANCE IN INDUSTRIAL ENVIRONMENTS**

Inventor(s): R. Halder, A. Majumder, S. Maity, P. Hait, A. Bhattacharyya, P. Satpathy, R. Sultana, K. Chakrabarti, D. Das, Assignee:

Published: 2025-04-04

Publication

1. **Performance Depiction of a Proficient pH-Sensor Employing Optimized Bilayer Tunnel Field Effect Transistor**

Amit Bhattacharyya, Ayush Sarkar, Nilanjana Sarkar, Pinaki Satpathy, Santanu Maity, Razia Sultana, Papiya Debnath, Manash Chanda,

Micro and Nanoelectronics Devices, Circuits and Systems (MNDCS 2025), organized by Dept. of ECE, NIT Silchar, Assam, India, in association with IEEE EDS NIT Silchar Student Branch Chapter and IEEE Nanotechnology Council Chapter., Volume , Year 2025, Pages

2. **Enhancing Reliability in DMF Biochips Operations Through Optimised Mixer Assignments And Mixing Modulation**

P. Satpathy, S. Maity, M. Mathur

Journal of Environmental Protection and Ecology, Volume 25, Year 2024, Pages 1649-1663

3. **Design and Implementation of Low Power 6T SRAM Using FINFET**

S. Maity, P. Satpathy, M. Mathur

Journal of Environmental Protection and Ecology, Volume 25, Year 2024, Pages 1664-1673

4. **Ventricular Dysfunction and Organ Transplant Rejection Determinant Biomarker Recognition in Serum via Charge Deduction Method**

Bhattacharyya A.; Nag P.; Maity S.; Satpathy P.; Debnath P.; Chanda M.

2024 15th International Conference on Computing Communication and Networking Technologies, ICCCNT 2024, Volume , Year 2024, Pages

5. **Design and Implementation of Low Power Carry Skip Adder**

Santanu Maity, Pinaki Satpathy

Lap Lambert Academic Publishing, Volume ISBN: 978-620-5-49293-2, Year 2022, Pages 72

6. **A Nobel Approach of designing a 4 Bit Carry Skip Adder using Reversible Logic**

P. Satpathy, S. Maity

Journal of VLSI Design Tools & Technology-JoVDTT (STM Journals), Volume 11, Year 2021, Pages 24-34

7. **Embedded Automobile Engine Locking System Using GSM Technology**
DEBARATI MANDAL, Argho Sarkar, Ayan Mukherjee, Debsona Mandal, Pinaki Satpathy
Journal of Microcontroller Engineering and Applications-JoMEA (STM Journals), Volume 06, Year 2019, Pages 1-7
8. **Implementation of Carry Select adder with Reduced Area Scheme**
P. Satpathy
Journal of VLSI Design Tools & Technology-JoVDTT (STM Journals), Volume 07, Year 2017, Pages 1-5
9. **Design and Implementation of Carry Select Adder using T-Spice**
Pinaki Satpathy
Hamburg, Anchor Academic Publishing, Germany, Volume ISBN: 978-3-96067-558-7, Year 2016, Pages 40
10. **Design and implementation of RFID based attendance system**
P. Satpathy
International Journal of Modern Embedded System-IJMES, Volume 4, Year 2016, Pages 1-5
11. **Design and Implementation of a digital code lock system using C-Programme**
P. Satpathy, S. Mukherjee, M. Jana, R. Maity
International Journal of Innovations in Engineering and Technology -IJJET, Volume 07, Year 2016, Pages 291-295
12. **Speaker Dependent Speech Recognition System Using Wavelet Transform**
Pinaki Satpathy
Lap Lambert Academic Publishing, Volume ISBN: 978-3-659-68002-1, Year 2015, Pages 52
13. **Effect Of Network Topology on Routing Protocol's Efficiency**
Akinchan Das., Pinaki Satpathy., Banibrata Bag.
Lap Lambert Academic Publishing, Volume ISBN: 978-3-659-67965-0, Year 2015, Pages 84
14. **Speed Checker on Highways for Accident Avoidance and Detection**
P. Satpathy, J. Mandal, K. Paul, K. Mondal, P. Naskar
International Journal of VLSI and Embedded Systems-IJVES, Volume 06, Year 2015, Pages 1509-1514
15. **An approach to demonstrate the fallacies of current fingerprint technology**
P. Satpathy, B.Bag, A.Das, R.Maity, M.Jana
International Journal of Innovative Research in Science, Engineering and Technology-IJIRSET, Volume 04, Year 2015, Pages 7371-7376
16. **GENERATING AN ISOLATED WORD RECOGNITION SYSTEM USING MATLAB**
P. Satpathy, A.Roy, K.Roy, R.Maity, S.Mukherjee
International Research Journal of Engineering and Technology (IRJET), Volume 02, Year 2015, Pages 906-910

17. Comprehensive analysis of delay in UDSM CMOS circuits

J.Samanta, R.K.Maity, B.P.De, P.Satpathy, D.Chowdhury

Proceedings of International Conference on Electronics Communication and Computing Technologies
2011, ICECCT'11, Volume , Year 2011, Pages 29-32

18. Design & Development of Array Multiplier for VLSI Application Using SPICE Simulation Tool

Surajit Mal, Siddhartha Sarkar, Subhadeep Mondal, Bishnu Prasad De, Jagannath Samanta, Pinaki
Satpathy and Banibrata Bag

National Seminar on Photonics and Nanosciencies, Volume , Year 2011, Pages 90-95